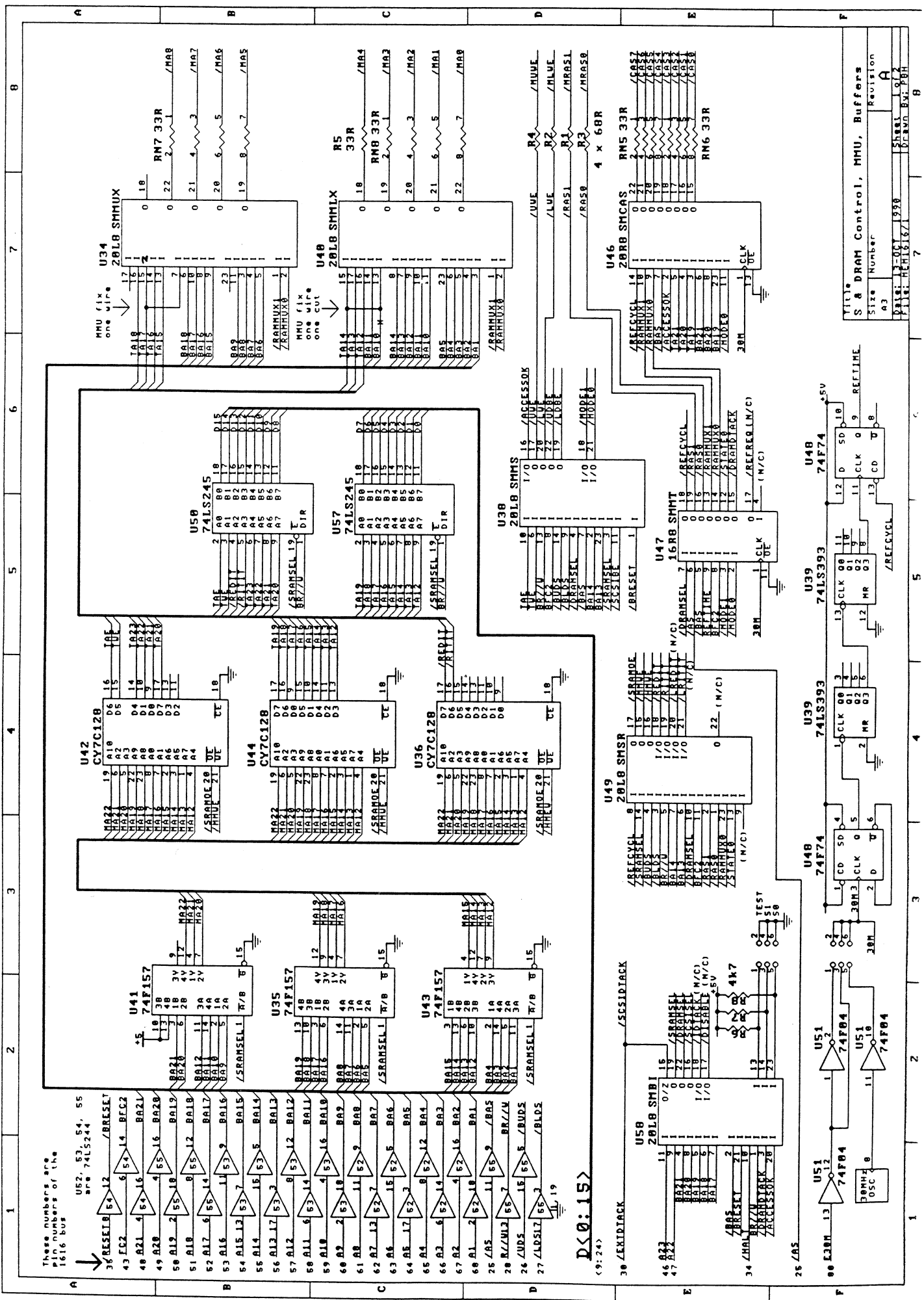


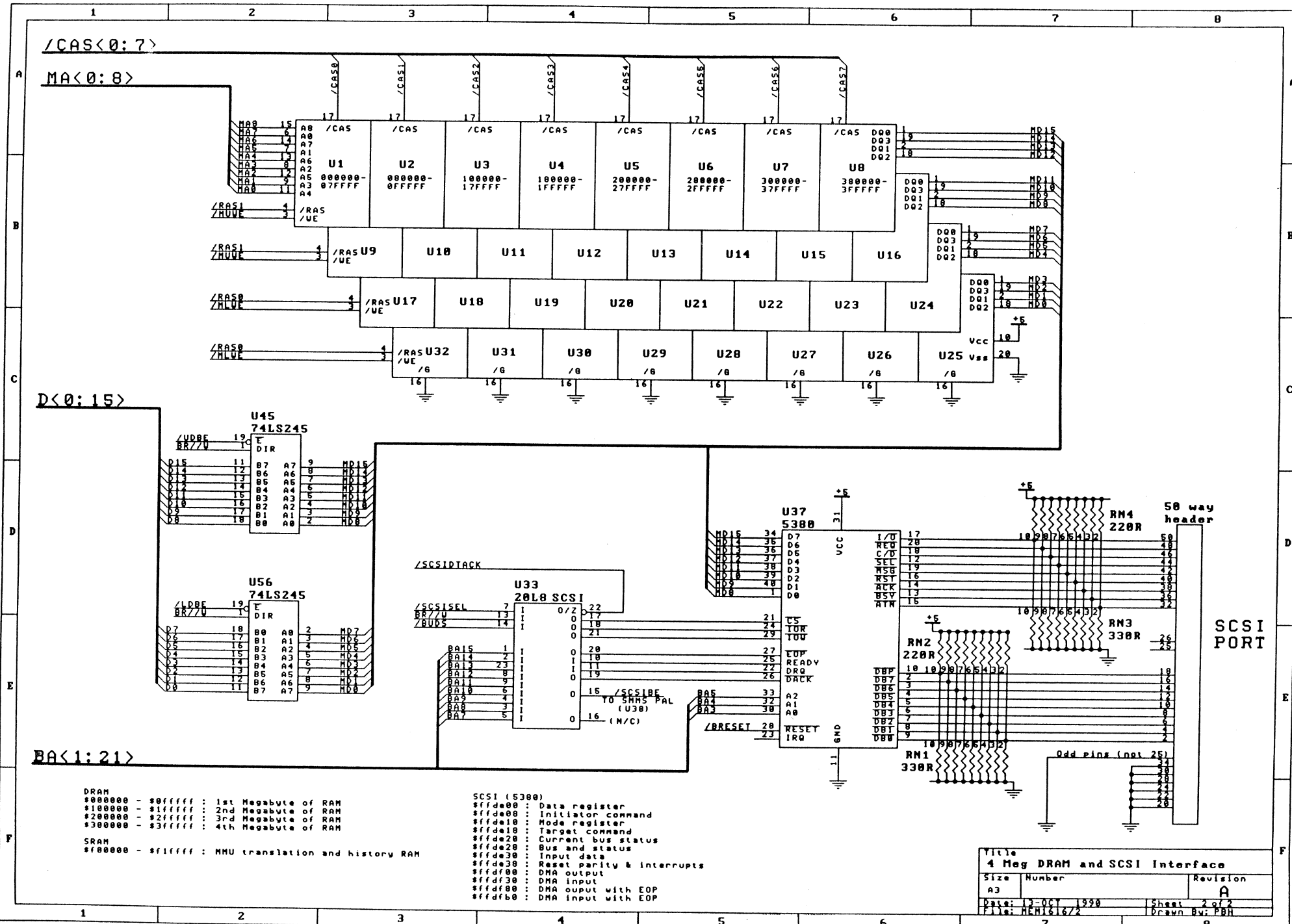
These numbers are pin numbers of the 1616 bus

U52, 53, 54, 55 are 74LS244



D<0:15>
(9:24)

Title: S & DRAM Control, MMU, Buffers
Size: A3
Revision: A
DATE: 2-01-93
DRAWN BY: BWH



/CAS<0:7>

MA<0:8>

D<0:15>

BA<1:21>

DRAM
 \$000000 - \$0ffffff : 1st Megabyte of RAM
 \$100000 - \$1ffffff : 2nd Megabyte of RAM
 \$200000 - \$2ffffff : 3rd Megabyte of RAM
 \$300000 - \$3ffffff : 4th Megabyte of RAM

SRAM
 \$f00000 - \$f1ffff : MMU translation and history RAM

SCSI (5380)
 \$ffde00 : Data register
 \$ffde08 : Initiator command
 \$ffde10 : Mode register
 \$ffde18 : Target command
 \$ffde20 : Current bus status
 \$ffde28 : Bus and status
 \$ffde30 : Input data
 \$ffde38 : Reset parity & interrupts
 \$ffdf00 : DMA output
 \$ffdf30 : DMA input
 \$ffdf80 : DMA output with EOP
 \$ffdfb0 : DMA input with EOP

Title		
4 Meg DRAM and SCSI Interface		
Size	Number	Revision
A3		A
Date:	13-OCT-1990	Sheet 2 of 2
File:	MEM16162	Drawn By: PBH