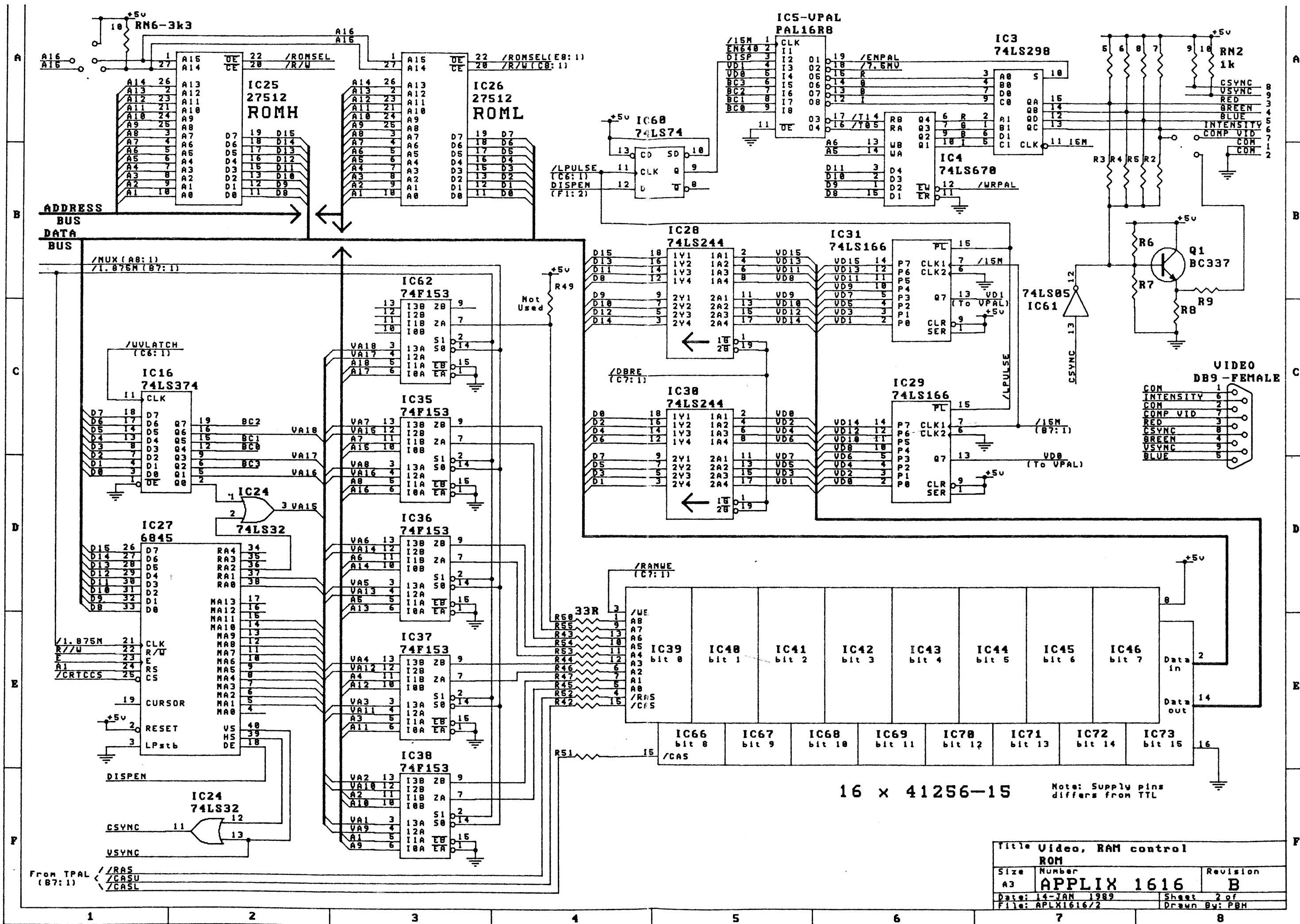


Title Processor, Timing, Address Decode & Interrupts		
Size	Number	Revision
A3	APPLIX 1616	B
Date: 14-JAN 1989	Sheet 1 of	
File: APLX1616/1	Drawn By: PBH	



16 x 41256-15

Note: Supply pins differs from TTL

Title Video, RAM control ROM		
Size	Number	Revision
A3	APPLIX 1616	B
Date: 14-JAN 1989	Sheet 2 of	Drawn By: PBH
File: APLX1616/2		

From TPAL (B7:1) /RAS /CASU /CASL